

WHAT IS CLAIMED IS:

1. An information processing apparatus comprising:  
an interface arranged to connect to a serial bus  
compatible to or complying with the IEEE 1394 standard;  
5 and  
a memory arranged to store, in a predetermined  
address area, configuration ROM information complying  
with the IEEE 1212 standard and to store configuration  
information identical to the configuration ROM  
10 information in an address area different from the  
predetermined address area.
2. The apparatus according to claim 1, wherein the  
configuration ROM information contains information used  
to refer to the address area where the configuration  
15 information is stored.
3. The apparatus according to claim 1, wherein  
the configuration ROM information has a general  
format defined in the IEEE 1212 standard, and  
the first entry in the root directory of the  
20 configuration ROM information contains information used  
to refer to the address area where the configuration  
information is stored.
4. The apparatus according to claim 1, wherein the  
address area where the configuration information is to  
25 be stored is an area where a block read transaction  
with a large block size is supported.
5. An information processing apparatus comprising:

an interface arranged to connect to a serial bus compatible to or complying with the IEEE 1394 standard; and

5 a memory arranged to store, in a predetermined address area, configuration ROM information with a minimal format complying with the IEEE 1212 standard and to store configuration information corresponding to configuration ROM information with a general format complying with the IEEE 1212 standard in an address  
10 area different from the predetermined address area.

6. The apparatus according to claim 5, wherein the configuration ROM information with the minimal format contains information used to refer to the address area where the configuration information is stored.

15 7. The apparatus according to claim 5, wherein the address area where the configuration information is to be stored is an area where a block read transaction with a large block size is supported.

8. An information processing method of processing  
20 information between a plurality of devices connected to a serial bus compatible to or complying with the IEEE 1394 standard, comprising the steps of:

reading out at least part of configuration ROM information complying with the IEEE 1212 standard,  
25 which is stored in a predetermined address area of a device; and

reading out configuration information identical

to the configuration ROM information, which is stored in an address area different from the predetermined address area, on the basis of the readout information.

9. The method according to claim 8, wherein the  
5 configuration ROM information contains information used to refer to the address area where the configuration information is stored.

10. The method according to claim 8, wherein  
the configuration ROM information has a general  
10 format defined in the IEEE 1212 standard, and  
the first entry in the root directory of the configuration ROM information contains information used to refer to the address area where the configuration information is stored.

15 11. The method according to claim 8, wherein the address area where the configuration information is to be stored is an area where a block read transaction with a large block size is supported.

12. An information processing method of processing  
20 information between a plurality of devices connected to a serial bus compatible to or complying with the IEEE 1394 standard, comprising the steps of:

reading out at least part of configuration ROM  
information with a minimal format complying with the  
25 IEEE 1212 standard, which is stored in a predetermined address area of a device; and

reading out information corresponding to

configuration ROM information with a general format according to the IEEE 1212 format, which is stored in an address area different from the predetermined address area, on the basis of the readout information.

5 13. The method according to claim 12, wherein the configuration ROM information contains information used to refer to the address area where the configuration information is stored.

10 14. The method according to claim 12, wherein the address area where the configuration information is to be stored is an area where a block read transaction with a large block size is supported.

15 15. A computer program product comprising a computer readable medium storing a computer program code, for an information processing method of processing information between a plurality of devices connected to a serial bus compatible to or complying with the IEEE 1394 standard, comprising process procedure code for:

20 reading out at least part of configuration ROM information complying with the IEEE 1212 standard, which is stored in a predetermined address area of a device; and

25 reading out configuration information identical to the configuration ROM information, which is stored in an address area different from the predetermined address area, on the basis of the readout information.

16. A computer program product comprising a computer

readable medium storing a computer program code, for an information processing method of processing information between a plurality of devices connected to a serial bus compatible to or complying with the IEEE 1394

5 standard, comprising process procedure code for:

reading out at least part of configuration ROM information with a minimal format complying with the IEEE 1212 standard, which is stored in a predetermined address area of a device; and

10 reading out information corresponding to configuration ROM information with a general format complying with the IEEE 1212 format, which is stored in an address area different from the predetermined address area, on the basis of the readout information.